DFA Node name

T0:

* 1 S' -> .CODE
* 3 CODE ->.
* 4 CODE -> .FDECL CODE
* 5 CODE -> .VDECL CODE
* 10 FDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi

T1:

* 2 S' -> CODE.

T2:

* 3 CODE ->.
* 4 CODE -> .FDECL CODE
* 5 CODE -> .VDECL CODE
* 8 CODE -> FDECL. CODE
* 10 FDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi

T3:

* 11 FDECL -> vtype. id lparen ARG rparen lbrace BLOCK RETURN rbrace
* 85 VDECL -> vtype. id semi
* 88 VDECL -> vtype. ASSIGN semi
* 91 ASSIGN -> .id assign RHS

T4:

* 3 CODE ->.
* 4 CODE -> .FDECL CODE
* 5 CODE -> .VDECL CODE
* 6 CODE -> VDECL. CODE
* 10 FDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi

T5:

* 9 CODE -> FDECL CODE.

T6:

* 12 FDECL -> vtype id. lparen ARG rparen lbrace BLOCK RETURN rbrace
* 86 VDECL -> vtype id. semi
* 92 ASSIGN -> id. assign RHS

T7:

* 89 VDECL -> vtype ASSIGN. semi

T8:

* 7 CODE -> VDECL CODE.

T9:

* 13 FDECL -> vtype id lparen. ARG rparen lbrace BLOCK RETURN rbrace
* 28 ARG -> .
* 29 ARG -> .vtype id MOREARGS

T10:

* 93 ASSIGN -> id assign. RHS
* 96 RHS -> .literal
* 97 RHS -> .EXPR
* 100 EXPR -> .TERM
* 101 EXPR -> .TERM addsub EXPR
* 106 TERM -> .FACTOR
* 107 TERM -> .FACTOR multdiv TERM
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T11:

* 87 VDECL -> vtype id semi.

T12:

* 90 VDECL -> vtype ASSIGN semi.

T13:

* 14 FDECL -> vtype id lparen ARG. rparen lbrace BLOCK RETURN rbrace

T14:

* 30 ARG -> vtype. id MOREARGS

T15:

* 94 ASSIGN -> id assign RHS.

T16:

* 95 RHS -> literal.

T17:

* 98 RHS -> EXPR .

T18:

* 99 EXPR -> TERM.
* 102 EXPR -> TERM. addsub EXPR

T19

* 105 TERM -> FACTOR.
* 108 TERM -> FACTOR. multdiv TERM

T20:

* 112 FACTOR -> float.

T21:

* 114 FACTOR -> num.

T22:

* 116 FACTOR -> id.

T23:

* 100 EXPR -> .TERM
* 101 EXPR -> .TERM addsub EXPR
* 106 TERM -> .FACTOR
* 107 TERM -> .FACTOR multdiv TERM
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen
* 118 FACTOR -> lparen. EXPR rparen

T24:

* 15 FDECL -> vtype id lparen ARG rparen. lbrace BLOCK RETURN rbrace

T25:

* 31 ARG -> vtype id. MOREARGS
* 33 MOREARGS -> .
* 34 MOREARGS -> .comma vtype id MOREARGS

T26:

* 100 EXPR -> .TERM
* 101 EXPR -> .TERM addsub EXPR
* 103 EXPR -> TERM addsub. EXPR
* 106 TERM -> .FACTOR
* 107 TERM -> .FACTOR multdiv TERM
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T27:

* 106 TERM -> .FACTOR
* 107 TERM -> .FACTOR multdiv TERM
* 109 TERM -> FACTOR multdiv. TERM
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T28:

* 119 FACTOR -> lparen EXPR. rparen

T29:

* 16 FDECL -> vtype id lparen ARG rparen lbrace. BLOCK RETURN rbrace
* 24 BLOCK -> .
* 25 BLOCK -> .STMT BLOCK
* 39 STMT -> .VDECL
* 40 STMT -> .ASSIGN semi
* 41 STMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE
* 42 STMT -> .while lparen COND rparen lbrace BLOCK rbrace
* 43 STMT -> .for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi
* 91 ASSIGN -> .id assign RHS

T30:

* 32 ARG -> vtype id MOREARGS.

T31:

* 35 MOREARGS -> comma. vtype id MOREARGS

T32:

* 104 EXPR -> TERM addsub EXPR.

T33:

* 110 TERM -> FACTOR multdiv TERM.

T34:

* 120 FACTOR -> lparen EXPR rparen.

T35:

* 17 FDECL -> vtype id lparen ARG rparen lbrace BLOCK. RETURN rbrace
* 20 RETURN -> .return FACTOR semi

T36:

* 24 BLOCK -> .
* 25 BLOCK -> .STMT BLOCK
* 26 BLOCK -> STMT. BLOCK
* 39 STMT -> .VDECL
* 40 STMT -> .ASSIGN semi
* 41 STMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE
* 42 STMT -> .while lparen COND rparen lbrace BLOCK rbrace
* 43 STMT -> .for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi
* 91 ASSIGN -> .id assign RHS

T37:

* 44 STMT -> VDECL.

T38:

* 45 STMT -> ASSIGN. Semi

T39:

* 47 STMT -> if. lparen COND rparen lbrace BLOCK rbrace ELSE

T40:

* 61 STMT -> while. lparen COND rparen lbrace BLOCK rbrace

T41:

* 72 STMT -> for. lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace

T42:

* 85 VDECL -> vtype. id semi
* 88 VDECL -> vtype. ASSIGN semi
* 91 ASSIGN -> .id assign RHS

T43:

* 92 ASSIGN -> id. assign RHS

T44:

* 36 MOREARGS -> comma vtype. id MOREARGS

T45:

* 18 FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN. rbrace

T46:

* 21 RETURN -> return. FACTOR semi
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T47:

* 27 BLOCK -> STMT BLOCK .

T48:

* 46 STMT -> ASSIGN semi.

T49:

* 48 STMT -> if lparen. COND rparen lbrace BLOCK rbrace ELSE
* 68 COND -> .FACTOR comp FACTOR
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T50:

* 62 STMT -> while lparen. COND rparen lbrace BLOCK rbrace
* 68 COND -> .FACTOR comp FACTOR
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T51

* 73 STMT -> for lparen. ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 91 ASSIGN -> .id assign RHS

T52

* 86 VDECL -> vtype id. semi
* 92 ASSIGN -> id. assign RHS

T53

* 33 MOREARGS -> .
* 34 MOREARGS -> .comma vtype id MOREARGS
* 37 MOREARGS -> comma vtype id. MOREARGS

T54:

* 19 FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace.

T55:

* 22 RETURN -> return FACTOR. semi

T56:

* 49 STMT -> if lparen COND. rparen lbrace BLOCK rbrace ELSE

T57:

* 69 COND -> FACTOR. comp FACTOR

T58:

* 63 STMT -> while lparen COND. rparen lbrace BLOCK rbrace

T59:

* 74 STMT -> for lparen ASSIGN. semi COND semi ASSIGN rparen lbrace BLOCK rbrace

T60:

* 38 MOREARGS -> comma vtype id MOREARGS.

T61:

* 23 RETURN -> return FACTOR semi.

T62:

* 50 STMT -> if lparen COND rparen. lbrace BLOCK rbrace ELSE

T63:

* 70 COND -> FACTOR comp. FACTOR
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T64:

* 64 STMT -> while lparen COND rparen. lbrace BLOCK rbrace

T65:

* 68 COND -> .FACTOR comp FACTOR
* 75 STMT -> for lparen ASSIGN semi. COND semi ASSIGN rparen lbrace BLOCK rbrace
* 111 FACTOR -> .float
* 113 FACTOR -> .num
* 115 FACTOR -> .id
* 117 FACTOR -> .lparen EXPR rparen

T66:

* 24 BLOCK -> .
* 25 BLOCK -> .STMT BLOCK
* 39 STMT -> .VDECL
* 40 STMT -> .ASSIGN semi
* 41 STMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE
* 42 STMT -> .while lparen COND rparen lbrace BLOCK rbrace
* 43 STMT -> .for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi
* 91 ASSIGN -> .id assign RHS

T67:

* 71 COND -> FACTOR comp FACTOR.

T68:

* 24 BLOCK -> .
* 25 BLOCK -> .STMT BLOCK
* 39 STMT -> .VDECL
* 40 STMT -> .ASSIGN semi
* 41 STMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE
* 42 STMT -> .while lparen COND rparen lbrace BLOCK rbrace
* 43 STMT -> .for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi
* 91 ASSIGN -> .id assign RHS

T69:

* 76 STMT -> for lparen ASSIGN semi COND. semi ASSIGN rparen lbrace BLOCK rbrace

T70:

* 52 STMT -> if lparen COND rparen lbrace BLOCK. rbrace ELSE

T72:

* 66 STMT -> while lparen COND rparen lbrace BLOCK. Rbrace

T72:

* 77 STMT -> for lparen ASSIGN semi COND semi. ASSIGN rparen lbrace BLOCK rbrace
* 91 ASSIGN -> .id assign RHS

T73

* 53 STMT -> if lparen COND rparen lbrace BLOCK rbrace. ELSE
* 55 ELSE -> .
* 56 ELSE -> .else lbrace BLOCK rbrace

T74:

* 67 STMT -> while lparen COND rparen lbrace BLOCK rbrace.

T75

* 78 STMT -> for lparen ASSIGN semi COND semi ASSIGN. rparen lbrace BLOCK rbrace

T76

* 54 STMT -> if lparen COND rparen lbrace BLOCK rbrace ELSE.

T77:

* 57 ELSE -> else. lbrace BLOCK rbrace

T78

* 79 STMT -> for lparen ASSIGN semi COND semi ASSIGN rparen. lbrace BLOCK rbrace

T79:

* 24 BLOCK -> .
* 25 BLOCK -> .STMT BLOCK
* 39 STMT -> .VDECL
* 40 STMT -> .ASSIGN semi
* 41 STMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE
* 42 STMT -> .while lparen COND rparen lbrace BLOCK rbrace
* 43 STMT -> .for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 58 ELSE -> else lbrace. BLOCK rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi
* 91 ASSIGN -> .id assign RHS

T80:

* 24 BLOCK -> .
* 25 BLOCK -> .STMT BLOCK
* 39 STMT -> .VDECL
* 40 STMT -> .ASSIGN semi
* 41 STMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE
* 42 STMT -> .while lparen COND rparen lbrace BLOCK rbrace
* 43 STMT -> .for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace
* 80 STMT -> for lparen ASSIGN semi COND semi ASSIGN rparen lbrace. BLOCK rbrace
* 83 VDECL -> .vtype ASSIGN semi
* 84 VDECL -> .vtype id semi
* 91 ASSIGN -> .id assign RHS

T81:

* 59 ELSE -> else lbrace BLOCK. rbrace

T82:

* 81 STMT -> for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK. rbrace

T83:

* 60 ELSE -> else lbrace BLOCK rbrace.

T84:

* 82 STMT -> for lparen ASSIGN semi COND semi ASSIGN rparen lbrace BLOCK rbrace.